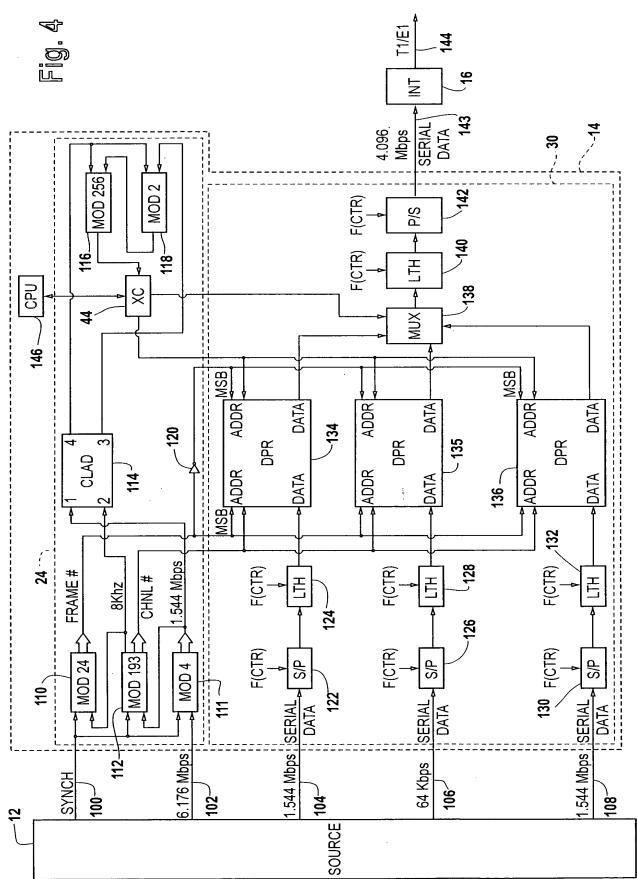
U.S. Patent Application No. 09/511,526 Filed: 02/23/2002 For: TIMESLOT INTERCHANGE CIRCUIT SUPPORTING PCM, ADPCM, AND MULTIPLE DATA CHANNEL.... Attorney Docket No. 6065-76257 Page 1 of 3 Figures 1-3 1/3 Fig. 1 10~ SYNCH **SYNCH** 18-T-CARRIER T-CARRIER **CROSS INTERFACE** 0 **SOURCE** CONNECT **DEVICE** 22-20 14 -12 -16 Fig. 2 14 -26 24-287 ADDR CONT **MULTIFRAME ADDR ADDR DUAL-PORT** SYNCH XC TBL MEMORY . **SYSTEM** 40 42 44 22 18 -**DATA** DATA -36 32 37 33~ DATA ر 20 34 38 Fig. 3 46 48 50 44 -1 20 2 40 7 3 42 1 3 4 40 0 0 o 21 42 3 22 42 18 23

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Figure 4

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Fig. 5

